

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	46376	(storage NEAR4 controller\$1)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/09 16:22
S4	777	S3 AND "707"/\$.ccls.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/09 16:25
S3	14721	(storage ADJ2 controller\$1)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/09 16:27
S6	80	S5 AND ("707"/\$.ccls. "709"/\$.ccls.)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/09 16:33
S5	270	S3 AND (storage ADJ2 node\$1)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 16:18
S7	1430	(storage ADJ1 controller\$1) AND (storage\$1 NEAR8 processor\$1 NEAR8 device\$1)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 16:19
S9	12	S8 AND "707"/\$.ccls.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 16:19
S8	53	S7 AND (storage\$1 NEAR10 (network\$1 NEAR4 address\$2))	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 16:22

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S10	14	S8 AND (map\$4 NEAR4 (network\$1 NEAR4 address\$2))	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 16:27
S11	10	("6574641" "6507883" "20040078465" "20040078466" "5870537").pn.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 16:29
S12	51	((storage\$1 NEAR4 (manager\$1 controller\$1)) AND (storage\$1 NEAR4 (node\$1 computer\$1)) AND (map\$4 NEAR4 address\$2)).clm.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 16:53
S14	54	S13 NOT S12	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 17:12
S15	1	"20040088297".pn.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/12 15:50
S16	45412	("707"/\$.ccls. "709"/\$.ccls. "718"/\$.ccls.)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/12 15:51
S17	97422	("707"/\$.ccls. "709"/\$.ccls. "718"/\$.ccls.)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/13 20:15
L1	97422	("707"/\$.ccls. "709"/\$.ccls. "718"/\$.ccls.)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/13 20:19
L2	6252	1 AND (storage\$1 NEAR4 (manager\$1 controller\$1))	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/13 20:19

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L3	480	2 AND (storage\$1 NEAR4 (node\$1 computer\$1)) AND ((conver\$5 chang\$4 translat\$4) NEAR4 address\$2)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/13 20:20
S13	67	((storage\$1 NEAR4 (manager\$1 controller\$1)) AND (storage\$1 NEAR4 (node\$1 computer\$1)) AND ((conver\$5 chang\$4 translat\$4) NEAR4 address\$2)).clm.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/13 20:20
L4	26	3 AND ((conver\$5 chang\$4 translat\$4) NEAR4 (address\$2 NEAR4 internet))	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/13 20:21

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1 [Simulation education: Perspectives on simulation in education and training: simulation models as an aid for the teaching and learning process in operations management](#)

Leonardo Chwif, Marcos Ribeiro Pereira Barretto

 December 2003 **Proceedings of the 35th conference on Winter simulation: driving innovation WSC '03**
Publisher: Winter Simulation Conference

 Full text available: [pdf\(485.99 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

As the focus of the teaching-learning process moves from teaching to learning, the need for a better understanding by teachers of the psychology of learning increases in relevance. Many studies have proved the differences between students' learning styles which leads to the need for different approaches to be adopted when teaching a subject. This article offers a brief review of learning styles to show how games can be applied, particularly in Operations Management. It also proposes a taxonom ...

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1 801 storage: architecture and programming



Albert Chang, Mark F. Mergen

February 1988 **ACM Transactions on Computer Systems (TOCS)**, Volume 6 Issue 1

Publisher: ACM Press

Full text available: pdf(1.87 MB)

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Based on novel architecture, the 801 minicomputer project has developed a low-level storage manager that can significantly simplify storage programming in subsystems and applications. The storage manager embodies three ideas: (1) large virtual storage, to contain all temporary data and permanent files for the active programs; (2) the innovation of database storage, which has implicit properties of access serializability and atomic update, similar to those of ...

2 Storage challenge: Storage challenge---Trading memory for disk: using parallel



access to fast InfiniBand disk arrays for large computational chemistry applications

Troy Benjegerdes, Brett Bode, Kyle Schochenmaier

November 2006 **Proceedings of the 2006 ACM/IEEE conference on Supercomputing SC '06**

Publisher: ACM Press

Full text available: [html\(2.19 KB\)](#)

Additional Information: full citation, abstract

We present a novel approach for using high performance network attached parallel storage for out-of-core computation. Our approach utilizes many parallel disks and storage controllers with a near 1:1 ratio of compute nodes to storage servers. This, when combined with 30 Gigabit 12X InfiniBand interconnects, allows remote storage subsystem bandwidth to reach the same performance level as local memory-cache file I/O performance. This combination allows computational chemistry application problems ...

3 Storage performance virtualization via throughput and latency control



Jianyong Zhang, Anand Sivasubramaniam, Qian Wang, Alma Riska, Erik Riedel

August 2006 **ACM Transactions on Storage (TOS)**, Volume 2 Issue 3

Publisher: ACM Press

Full text available: pdf(523.67 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

I/O consolidation is a growing trend in production environments due to increasing complexity in tuning and managing storage systems. A consequence of this trend is the need to serve multiple users and/or workloads simultaneously. It is imperative to ensure



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» Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **A high-performance two-stage packet switch architecture**
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